



Patent  
52478-5600

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shuichi Takayama *et al.*

Application No.: 09/301,284

Filed: April 28, 1999

For: PROCESSOR FOR EXECUTING  
INSTRUCTIONS IN UNITS THAT  
ARE UNRELATED TO THE UNITS IN  
WHICH INSTRUCTIONS ARE READ  
AND A COMPILER, AN  
OPTIMIZATION APPARATUS, AN  
ASSEMBLER, A LINKER, A  
DEBUGGER, AND A  
DISASSEMBLER FOR SUCH A  
DEBUGGER

Examiner: Ted T. Vo

Group Art Unit: 2122

July 7, 2003

Irvine, California 92614-7230

*Amend C*  
*#24*  
*T.D.*  
*07/18/03*

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Technology Center 2100

**RESPONSE AND AMENDMENT TO THE OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

Sir:

In response to the Office Action mailed January 9, 2003, Applicants submit the following claims and arguments. Please amend the above-identified application for patent as follows. Amendments in this Response are presented in the following pages in compliance with the revised amendment format of the proposed revision to 37 C.F.R. § 1.121.

Claims 1 and 49 have been amended and claim 50 has been added. Claims 1-11, 49 and 50 remain pending in the application.